

FIG. 2

ENCODING/DECODING MATRIX REPRESENTATION 1:5 ENCODING

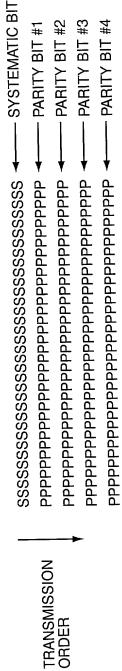


FIG. 3

TRANSMISSION TEMPLATE FOR ENCODING/DECODING

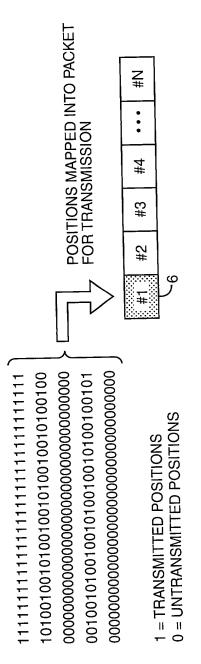
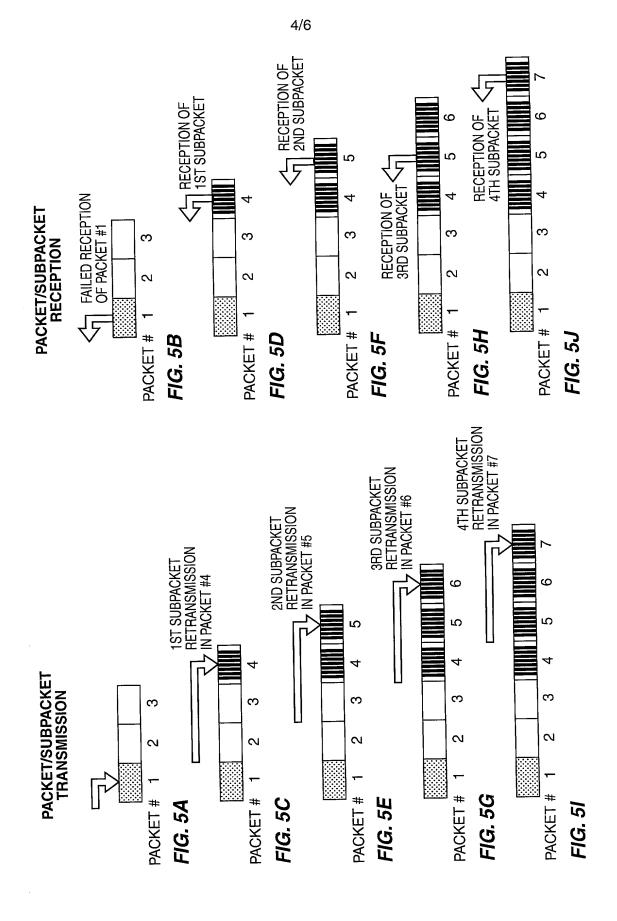


FIG. 4



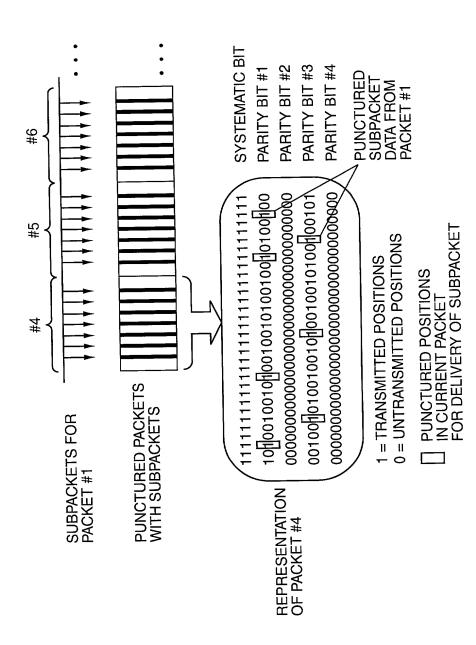
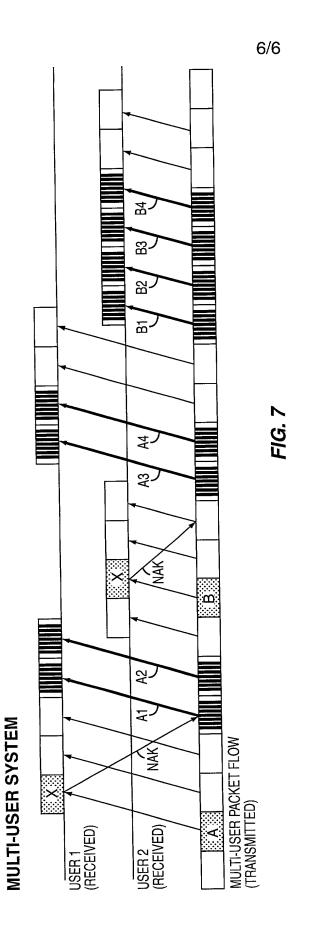


FIG. 6



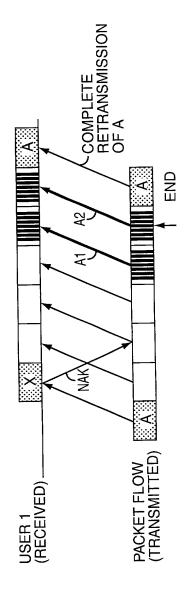


FIG. 8

FIG. 1

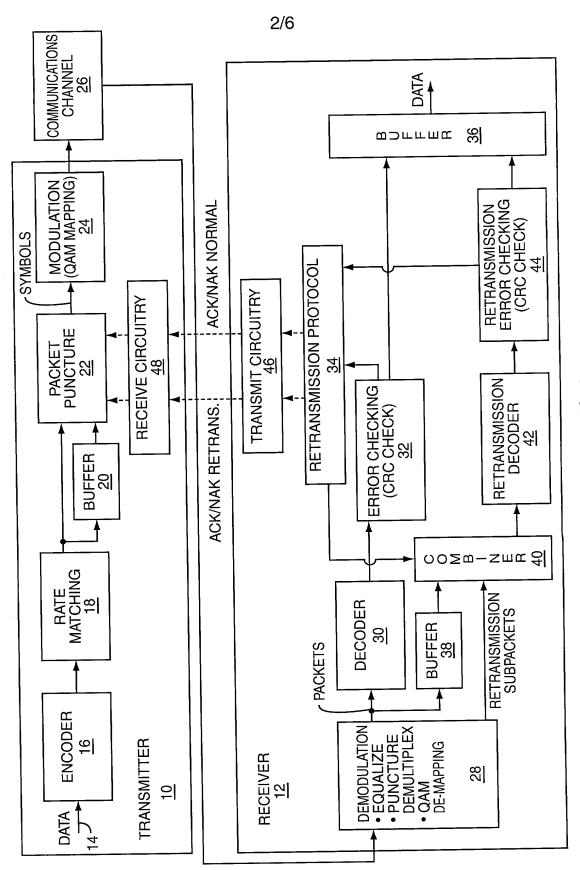


FIG. 2

ENCODING/DECODING MATRIX REPRESENTATION 1:5 ENCODING

SYSTEMATIC BIT PARITY BIT #2 PARITY BIT #3 PARITY BIT #4 PARITY BIT #1 ddddddddddddddddddddddddddddd ddddddddddddddddddddddddddddd dddddddddddddddddddddddddddddd ddddddddddddddddddddddddddddd TRANSMISSION ORDER

FIG. 3

TRANSMISSION TEMPLATE FOR ENCODING/DECODING

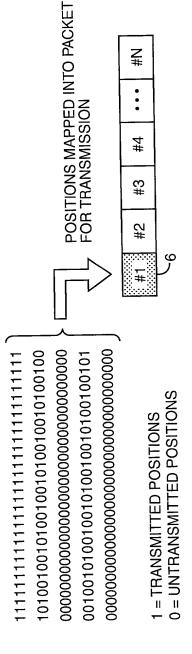
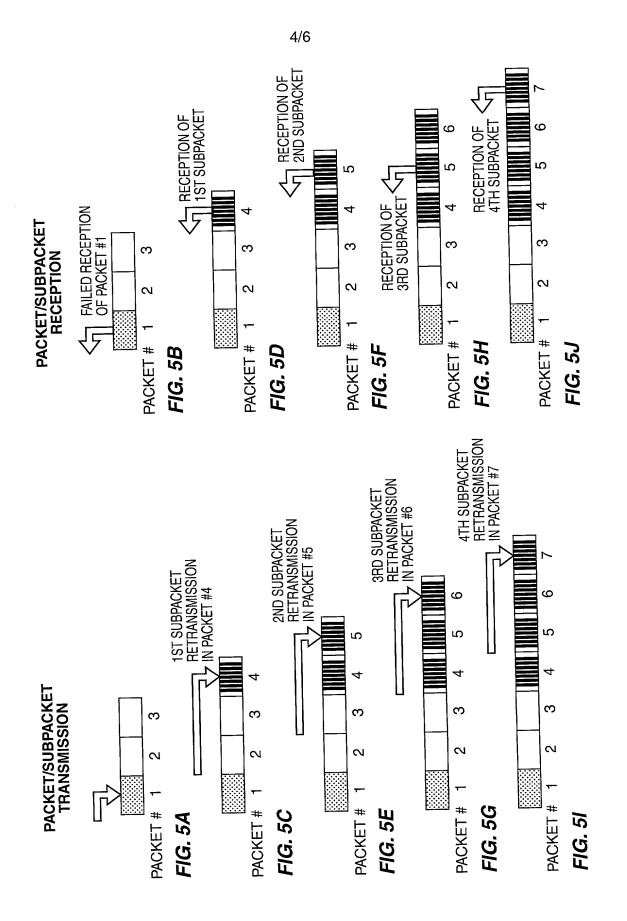


FIG. 4



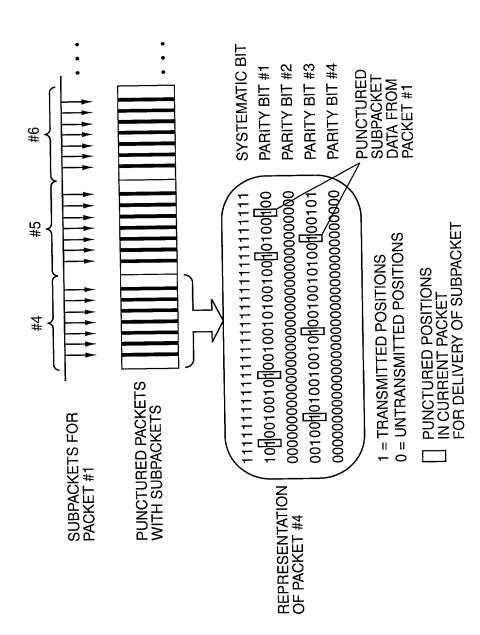
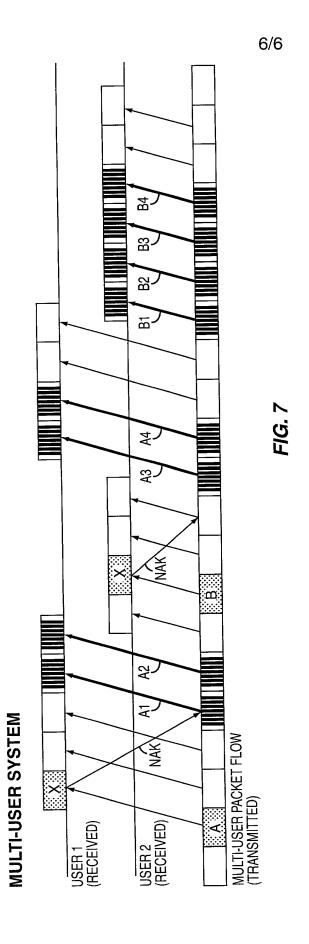


FIG. 6



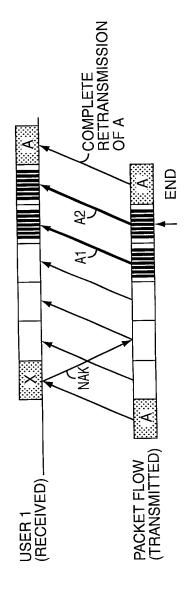
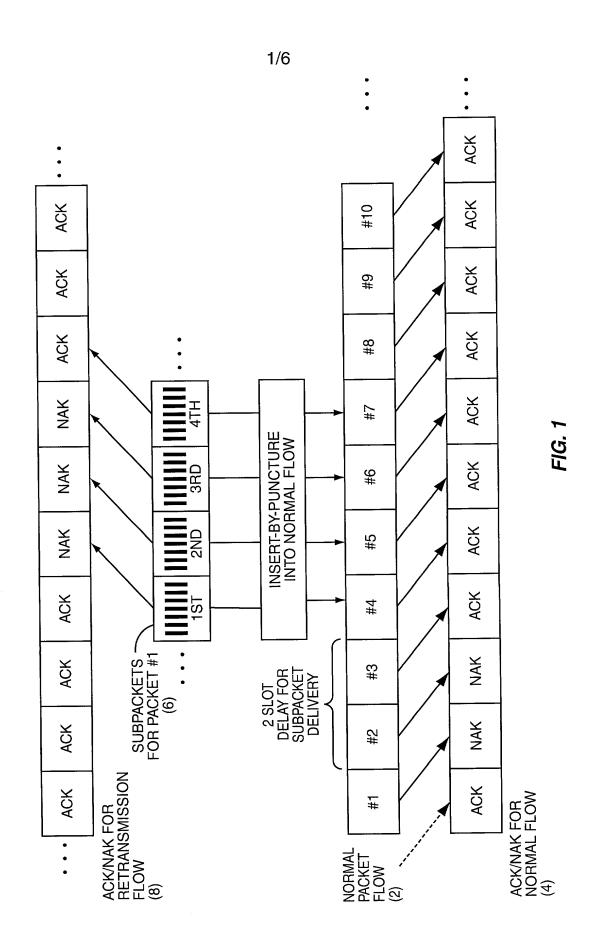


FIG. 8



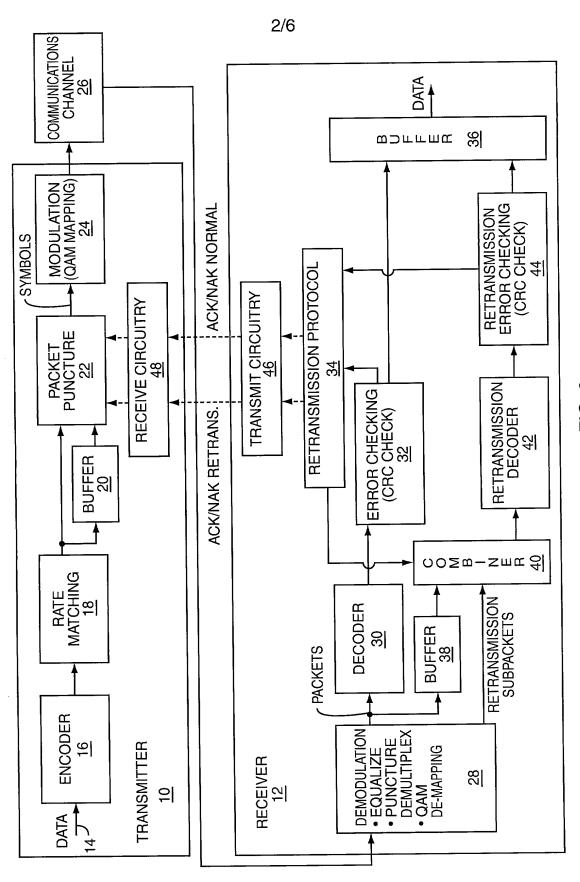


FIG. 2

ENCODING/DECODING MATRIX REPRESENTATION 1:5 ENCODING



SYSTEMATIC BIT

PARITY BIT #2 PARITY BIT #3 PARITY BIT #4

PARITY BIT #1

FIG. 3

TRANSMISSION TEMPLATE FOR ENCODING/DECODING

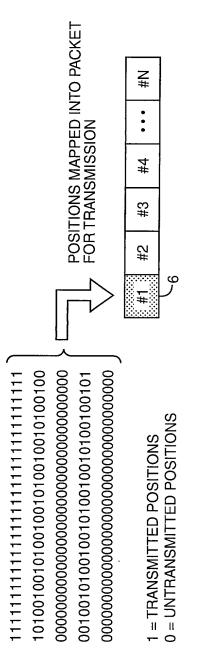
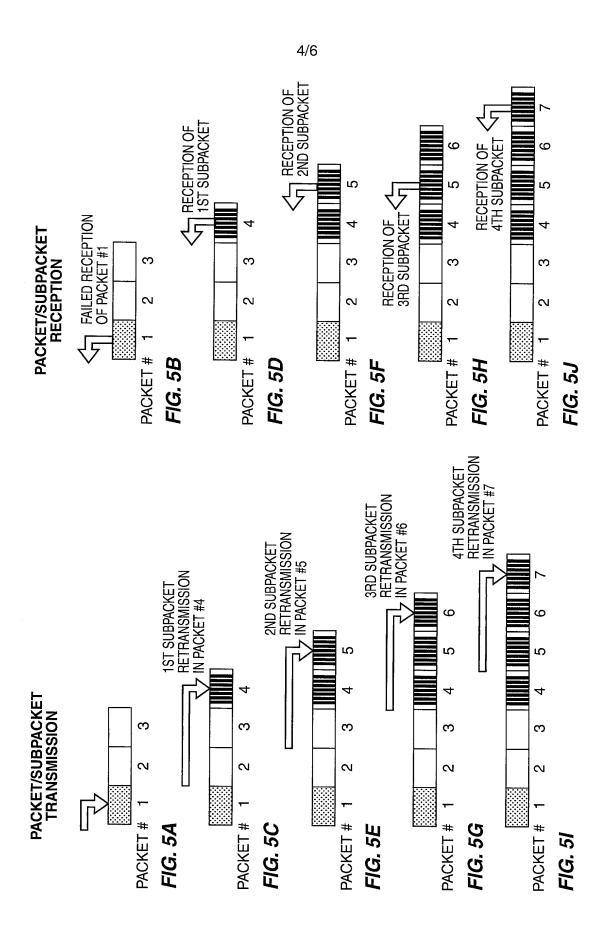


FIG. 4



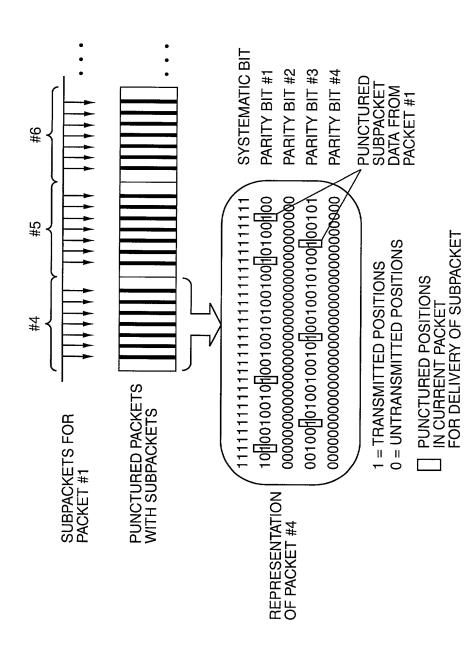


FIG. 6

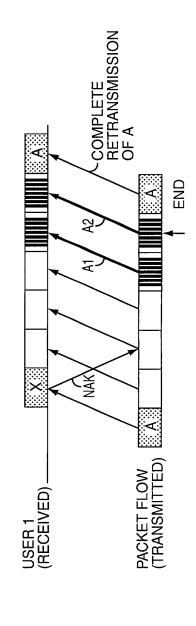


FIG. 8